

Digital Integrated Circuits A Design Perspective

Jan M. Rabaey

Digital Integrated Circuits Jan M. Rabaey, Anantha P. Chandrakasan, Borivoje Nikolić, 2003 Intended for use in undergraduate senior-level digital circuit design courses with advanced material sufficient for graduate-level courses. Progressive in content and form, this text successfully bridges the gap between the circuit perspective and system perspective of digital integrated circuit design. Beginning with solid discussions on the operation of electronic devices and in-depth analysis of the nucleus of digital design, the text maintains a consistent, logical flow of subject matter throughout. The revision addresses today's most significant and compelling industry topics, including: the impact of interconnect, design for low power, issues in timing and clocking, design methodologies, and the tremendous effect of design automation on the digital design perspective. The revision reflects the ongoing evolution in digital integrated circuit design, especially with respect to the impact of moving into the deep-submicron realm.

Digital Integrated Circuits Jan M. Rabaey, 2003

Digital Integrated Circuits Jan M. Rabaey, 1996 Contemporary in content and form, this practical book successfully bridges the gap between the circuit perspective and system perspective of digital integrated circuit design. Beginning with a solid foundation of the operation of electronic devices and an in-depth analysis of the nucleus of digital design, it maintains a consistent, logical flow of subject matter throughout, addressing today's most significant and compelling industry topics: the effect of interconnect, design for low power, issues in timing and clocking, design methodologies, and the tremendous impact of design automation on the digital design perspective. Discusses state-of-the-art topics in design, such as complex gates, adders, multipliers, registers, controllers and memories. Focuses on practical design issues, with examples, design problems and case studies creating practical knowledge readers can readily apply in industrial design. Offers perspectives on the future evolution of design practice at the end of each chapter to put issues discussed in a broader vision. Includes many illustrations and reality-based design problems to foster comprehension. For professional engineers.

Digital Integrated Circuits : Design Perspective(2nd)(Paperback) Jan M. Rabaey, 2012-01-01

Digital Integrated Circuits Evgeni Perelroyzen, 2018-10-03 A current trend in digital design-the integration of the MATLAB® components Simulink® and Stateflow® for model building, simulations, system testing, and fault detection-allows for better control over the design flow process and, ultimately, for better system results. Digital Integrated Circuits: Design-

for-Test Using Simulink® and Stateflow® illustrates the construction of Simulink models for digital project test benches in certain design-for-test fields. The first two chapters of the book describe the major tools used for design-for-test. The author explains the process of Simulink model building, presents the main library blocks of Simulink, and examines the development of finite-state machine modeling using Stateflow diagrams. Subsequent chapters provide examples of Simulink modeling and simulation for the latest design-for-test fields, including combinational and sequential circuits, controllability, and observability; deterministic algorithms; digital circuit dynamics; timing verification; built-in self-test (BIST) architecture; scan cell operations; and functional and diagnostic testing. The book also discusses the automatic test pattern generation (ATPG) process, the logical determinant theory, and joint test action group (JTAG) interface models. Digital Integrated Circuits explores the possibilities of MATLAB's tools in the development of application-specific integrated circuit (ASIC) design systems. The book shows how to incorporate Simulink and Stateflow into the process of modern digital design.

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CMOS Digital Integrated Circuits Sung-Mo Kang, Yusuf Leblebici, 1999 The second edition of this comprehensive text contains extensive revisions to reflect recent advances in technology and in circuit design practices. Recognizing that the area of digital integrated circuit design is evolving at an increasingly fast pace, every effort has been made to present state-of-the-art material on all subjects covered in the book. This book is primarily designed as a comprehensive text for senior level and first-year graduate level digital circuit design classes, as well as a reference for practicing engineers in the areas of IC design and VLSI.

Handbook of Integrated Circuit Industry Yangyuan Wang, Min-Hwa Chi, Jesse Jen-Chung Lou, Chun-Zhang Chen, 2023-11-27 Written by hundreds of experts who have made contributions to both enterprise and academic research, these excellent reference books provide all necessary knowledge of the whole industrial chain of integrated circuits, and cover topics related to the technology evolution trends, fabrication, applications, new materials, equipment, economy, investment, and industrial developments of integrated circuits. Especially, the coverage is broad in scope and deep enough for all kinds of readers being interested in integrated circuit industry. Remarkable data collection, update marketing evaluation, enough working knowledge of integrated circuit fabrication, clear and accessible category of integrated circuit products, and good equipment insight explanation, etc. can make general readers build up a clear overview about the whole integrated circuit industry. This encyclopedia is designed as a reference book for scientists and engineers actively involved in integrated circuit research and development field. In addition, this book provides enough guide lines and knowledges to

benefit enterprisers being interested in integrated circuit industry.

Nanotechnology Shilpi Birla, Neha Singh, Neeraj Kumar Shukla, 2022-03-02 This reference text discusses recent advances in the field of nanotechnology with applications in the fields of electronics sector, agriculture, health services, smart cities, food industry, and energy sector in a comprehensive manner. The text begins by discussing important concepts including bio nanotechnology, nano electronics, nano devices, nano medicine, and nano memories. It then comprehensively covers applications of nanotechnology in different areas including healthcare, energy sector, environment, security and defense, agriculture sector, food industry, automotive sector, smart cities, and Internet of Things (IoT). Aimed at senior undergraduate, graduate students and professionals in the fields of electrical engineering, electronics engineering, nanoscience and nanotechnology, this text: Discusses nano image sensors useful for imaging in medical and for security applications. Covers advances in the field of nanotechnology with their applications. It covers important concepts including neuro simulators, nano medicine, and nano materials. Covers applications of nanotechnology in diverse fields including health sector, agriculture, energy sector, and electronics.

Design of High Voltage xDSL Line Drivers in Standard CMOS Bert Serneels, Michiel Steyaert, 2008-01-08 “Design of high voltage xDSL line drivers in standard CMOS” fits in the quest for highly efficient fully integrated xDSL modems for central office applications. The book focusses on the line driver, the most demanding building block of the xDSL modem for lowering power. To reduce the cost, the cheapest technology is selected: standard CMOS, without any extra process options to increase the nominal supply voltage. The emphasis lies on the analysis, design and implementation of high voltage highly efficient line drivers in mainstream CMOS. “Design of high voltage xDSL line drivers in standard CMOS” covers the total design flow of monolithic CMOS high voltage circuits. The book is essential reading for analog design engineers and researchers in the field and is also suitable as a text book for an advanced course on the subject.

The VLSI Handbook Wai-Kai Chen, 2018-10-03 For the new millenium, Wai-Kai Chen introduced a monumental reference for the design, analysis, and prediction of VLSI circuits: The VLSI Handbook. Still a valuable tool for dealing with the most dynamic field in engineering, this second edition includes 13 sections comprising nearly 100 chapters focused on the key concepts, models, and equations. Written by a stellar international panel of expert contributors, this handbook is a reliable, comprehensive resource for real answers to practical problems. It emphasizes fundamental theory underlying professional applications and also reflects key areas of industrial and research focus. WHAT'S IN THE SECOND EDITION? Sections on... Low-power electronics and design VLSI signal processing Chapters on... CMOS fabrication Content-addressable memory Compound semiconductor RF circuits High-speed circuit design principles SiGe HBT technology Bipolar junction transistor amplifiers Performance modeling and analysis using SystemC Design languages, expanded from two chapters to twelve Testing of digital systems Structured for convenient navigation and loaded with practical solutions, The VLSI Handbook,

Second Edition remains the first choice for answers to the problems and challenges faced daily in engineering practice.

Analysis and Design of Networks-on-Chip Under High Process Variation Rabab Ezz-Eldin, Magdy Ali El-Moursy, Hesham F. A. Hamed, 2015-12-16 This book describes in detail the impact of process variations on Network-on-Chip (NoC) performance. The authors evaluate various NoC topologies under high process variation and explain the design of efficient NoCs, with advanced technologies. The discussion includes variation in logic and interconnect, in order to evaluate the delay and throughput variation with different NoC topologies. The authors describe an asynchronous router, as a robust design to mitigate the impact of process variation in NoCs and the performance of different routing algorithms is determined with/without process variation for various traffic patterns. Additionally, a novel Process variation Delay and Congestion aware Routing algorithm (PDCR) is described for asynchronous NoC design, which outperforms different adaptive routing algorithms in the average delay and saturation throughput for various traffic patterns.

Advanced Topics in Microelectronics and System Design Giuseppe Ferla, Luigi Fortuna, Antonio Imbruglia, 2000 This volume covers a wide area ? from research topics to the design and improvement of integrated circuit devices, already existing or to be introduced to the market.

Variation Tolerant On-Chip Interconnects Ethiopia Enideg Nigussie, 2011-12-02 This book presents design techniques, analysis and implementation of high performance and power efficient, variation tolerant on-chip interconnects. Given the design paradigm shift to multi-core, interconnect-centric designs and the increase in sources of variability and their impact in sub-100nm technologies, this book will be an invaluable reference for anyone concerned with the design of next generation, high-performance electronics systems.

Low Power Design Essentials Jan Rabaey, 2009-04-21 Low Power Design Essentials contains all the topics of importance to the low power designer. The book lays the foundation with background chapters entitled “Advanced MOS Transistors and Their Models” and “Power Basics”. These chapters are followed by chapters on the design process including: optimization, architecture and algorithm level, memory, run time, standby logic, and standby memory. Chapters on special topics are also included: power management and modal design, ultra low power, and low power design methodology and flows. The book concludes with a chapter on case studies as well as a chapter on “Projection into the Future”. These chapters are all based on the extensive amount of teaching that the author has carried out both at universities and companies worldwide. All chapters have been drawn up specifically for self-study. They aim, however, at different levels of understanding. All the chapters start with elementary material, but most also contain advanced material.

Education and Educational Technology Yuanzhi Wang, 2011-10-07 This volume includes extended and revised versions of a set of selected papers from the 2011 2nd International Conference on Education and Educational Technology (EET 2011) held in Chengdu, China, October 1-2, 2011. The mission of EET 2011 Volume 1 is to provide a forum for

researchers, educators, engineers, and government officials involved in the general areas of education and educational technology to disseminate their latest research results and exchange views on the future research directions of these fields. 130 related topic papers were selected into this volume. All the papers were reviewed by 2 program committee members and selected by the volume editor Prof. Yuanzhi Wang, from Intelligent Information Technology Application Research Association, Hong Kong. The conference will bring together leading researchers, engineers and scientists in the domain of interest. We hope every participant can have a good opportunity to exchange their research ideas and results and to discuss the state of the art in the areas of the education and educational technology.

Wireless Transceiver Systems Design Wolfgang Eberle, 2008-06-17 1 During the last 30 years, wireless in communications has grown from a niche market to an economically vital consumer mass market. The first wave, with the breakthrough of 2G mobile telephony focused on speech, placed wireless communication in the consumer mass market. In the current second wave, services are extended toward true multimedia, including interactive video, audio, gaming, and broadband Internet. These high-data rate services, however, led to a separate IP-centric family of wireless personal (WPANs) and local area networks (WLANs) outside the 2G/3G mobile path. Since diversity between data- and voice-centric solutions and the competition between standardized and proprietary approaches is today more blocking than enabling effective development of successful products, a third major wave is unavoidable: a consolidation of both worlds in portable devices with flexible multistandard communication capabilities enabled for quality-of-service- 2 aware multimedia services. At the same time, the dominance of wired desktop personal computers has been undermined by the appearance of numerous portable and smart devices: laptops, notebooks, personal digital assistants, and gaming devices. Since these devices target low-cost consumer markets or face wired competition, time to market is crucial, designed-in flexibility is important, low-power operation is a key asset, yet device cost shall be at a minimum. This book approaches this design tradeoff challenge from the perspective of the system architect. The system architect is concerned both in an efficient design process and in a competitive design result.

Design based Research Kirat Pal Singh, Author Impact

Design Through Verilog HDL T. R. Padmanabhan, B. Bala Tripura Sundari, 2003-11-05 A comprehensive resource on Verilog HDL for beginners and experts Large and complicated digital circuits can be incorporated into hardware by using Verilog, a hardware description language (HDL). A designer aspiring to master this versatile language must first become familiar with its constructs, practice their use in real applications, and apply them in combinations in order to be successful. Design Through Verilog HDL affords novices the opportunity to perform all of these tasks, while also offering seasoned professionals a comprehensive resource on this dynamic tool. Describing a design using Verilog is only half the story: writing test-benches, testing a design for all its desired functions, and how identifying and removing the faults remain significant

challenges. Design Through Verilog HDL addresses each of these issues concisely and effectively. The authors discuss constructs through illustrative examples that are tested with popular simulation packages, ensuring the subject matter remains practically relevant. Other important topics covered include: Primitives Gate and Net delays Buffers CMOS switches State machine design Further, the authors focus on illuminating the differences between gate level, data flow, and behavioral styles of Verilog, a critical distinction for designers. The book's final chapters deal with advanced topics such as timescales, parameters and related constructs, queues, and switch level design. Each chapter concludes with exercises that both ensure readers have mastered the present material and stimulate readers to explore avenues of their own choosing. Written and assembled in a paced, logical manner, Design Through Verilog HDL provides professionals, graduate students, and advanced undergraduates with a one-of-a-kind resource.

Adiabatic Logic Philip Teichmann, 2011-10-29 Adiabatic logic is a potential successor for static CMOS circuit design when it comes to ultra-low-power energy consumption. Future development like the evolutionary shrinking of the minimum feature size as well as revolutionary novel transistor concepts will change the gate level savings gained by adiabatic logic. In addition, the impact of worsening degradation effects has to be considered in the design of adiabatic circuits. The impact of the technology trends on the figures of merit of adiabatic logic, energy saving potential and optimum operating frequency, are investigated, as well as degradation related issues. Adiabatic logic benefits from future devices, is not susceptible to Hot Carrier Injection, and shows less impact of Bias Temperature Instability than static CMOS circuits. Major interest also lies on the efficient generation of the applied power-clock signal. This oscillating power supply can be used to save energy in short idle times by disconnecting circuits. An efficient way to generate the power-clock is by means of the synchronous 2N2P LC oscillator, which is also robust with respect to pattern-induced capacitive variations. An easy to implement but powerful power-clock gating supplement is proposed by gating the synchronization signals. Diverse implementations to shut down the system are presented and rated for their applicability and other aspects like energy reduction capability and data retention. Advantageous usage of adiabatic logic requires compact and efficient arithmetic structures. A broad variety of adder structures and a Coordinate Rotation Digital Computer are compared and rated according to energy consumption and area usage, and the resulting energy saving potential against static CMOS proves the ultra-low-power capability of adiabatic logic. In the end, a new circuit topology has to compete with static CMOS also in productivity. On a 130nm test chip, a large scale test vehicle containing an FIR filter was implemented in adiabatic logic, utilizing a standard, library-based design flow, fabricated, measured and compared to simulations of a static CMOS counterpart, with measured saving factors compliant to the values gained by simulation. This leads to the conclusion that adiabatic logic is ready for productive design due to compatibility not only to CMOS technology, but also to electronic design automation (EDA) tools developed for static CMOS system design.

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