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**Padhraic Smyth**

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web lfsr stands for linear feedback shift register and it is a design that is useful inside of fpgas lfsrs are simple to synthesize meaning that they take relatively few resources and can be run at very high clock rates inside of an fpga there are many applications that benefit from using an lfsr including counters test pattern generators

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web the present lfsr linear feedback shift register is modified by using the transition controller module the transition controller is utilized to raise the interrelation between successive test patterns

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lfsr fewer gates higher clock frequency  
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